* Reference appendices more
  1. Reference sections, like user interfaces, with appropriate appendix
* 2.4 General Constraints
  1. Ambiguous, remove it or spell it out more
  2. Client does not need to know things like creating custom component
  3. Think about more constraints, like storage constraints
* 3.2.3.2 Inputs
* Front end and back end has no definition, and is referenced a lot. It is also part of the design, the detail if which part does what isn’t necessary to be told to the client
* 3.4.2.3 Register display – tell which registers are going to be displayed
* 3.2.4.3 Processing – Not parsing in low level – assembling or translating.
  1. Builds an object program, maintains a location counter, etc.
* Need format of .PRT file in appendix
* Can look like the original, but can leave off address columns
  1. Want LOC Object Code Line# Source Statement
* Architectural design not needed in Spec doc
  1. Remove section 3.4 and put in a design doc
* Line 445: Reserving the right leaves it as us completely in control - needs to be negotiable later, not us having full decisions
* Line 476: Amended by approval, not just updated anytime any way we want
* Nothing in appendix should be there unless its supporting something in the document
  1. Appendix should be reference much more
* Needs error messages in appendix
* Plan for testing
  1. Add a testing section
  2. Add how we are going to specify how it’s going to be tested and reported
* Not needed in document, but must show a final color scheme before signing
* Colors are good, but it needs to be larger (full screen preferred)
* Try to have changes by late Sunday /early Monday

2 pass assembler:

* Pass 1:
  1. Maintain location counter
     + Make use of the machine op table
     + Process DS and DC statements
       - Know sizes
         * F=4
         * D=8
         * H=2
         * C=1
       - Enforce boundary alignments
     + Process literals
       - Literal’s value is its name
       - Create a literal table
  2. Construct the Symbol Table
  3. Optional: Create an intermediate file for pass 2
  4. Process assembler directives
     + Start
     + End
     + Using
     + DS/DC
     + SPACE may be postponed
     + TITLE may be postponed
     + EJECT may be postponed
* After pass 1, will not know of any errors in operand field, only in operation/label field
* Pass 2: Using the intermediate file from pass 1 or the original source file
  1. Create object code for each line
     + Use machine op table
     + Use symbol table
     + Use the literal table
  2. Create a listing line in .PRT file

LOC Object Code Line# Source Statement

* 1. Print any errors in pass 1 or 2
  2. Create an object program